

IN THE CLAIMS

Please amend the claims as follows.

1. (Currently Amended) A method for manufacturing an interconnected circuit board assembly, the method comprising:

fabricating a first circuit board having one or more first bond pads;

fabricating a second circuit board having multiple optoelectrical display elements and one or more second bond pads electrically connected to the multiple optoelectrical display elements;

placing one or more spacers on the one or more first bond pads of [[a]] the first circuit board, wherein the one or more spacers are formed of a conductive material that remains in a solid form during attachment of the first circuit board to a second circuit board;

aligning the first circuit board with the second circuit board by engaging the spacers with openings in the second circuit board so that the one or more second bond pads of the second circuit board align with the one or more first bond pads, and the one or more second bond pads make electrical contact with the one or more spacers; and

attaching the first circuit board to the second circuit board.

2. (Original) The method as claimed in claim 1, wherein placing the one or more spacers comprises attaching the one or more spacers to the one or more first bond pads by forming the one or more spacers on the one or more first bond pads using a selective electroplating process.

3. (Original) The method as claimed in claim 1, wherein placing the one or more spacers comprises attaching the one or more spacers to the one or more first bond pads by welding the one or more spacers to the one or more first bond pads.

4. (Original) The method as claimed in claim 1, wherein placing the one or more spacers comprises attaching the one or more spacers to the one or more first bond pads by applying solder to the one or more first bond pads.

5. (Original) The method as claimed in claim 1, further comprising, prior to aligning, applying a conductive material in proximity to areas where each of the one or more spacers will contact complementary ones of the one or more second bond pads.
6. (Original) The method as claimed in claim 5, wherein applying the conductive material comprises applying a conductive material in contact with each of the one or more spacers.
7. (Original) The method as claimed in claim 5, wherein the conductive material is a conductive paste.
8. (Original) The method as claimed in claim 5, wherein the conductive material is solder.
9. (Original) The method as claimed in claim 5, wherein attaching the first circuit board to the second circuit board comprises heating the conductive material.
10. (Original) The method as claimed in claim 1, further comprising inserting an insulating material in an interface region between the first circuit board and the second circuit board.
11. (Original) The method as claimed in claim 10, wherein inserting the insulating material comprises injecting the insulating material into the interface region by a vacuum fill process after attaching the first circuit board to the second circuit board.
12. (Original) The method as claimed in claim 10, wherein inserting the insulating material comprises applying the insulating material to one of the first circuit board and the second circuit board, followed by partially curing the insulating material, prior to attaching the first circuit board to the second circuit board.
13. (Original) The method as claimed in claim 12, further comprising fully curing the insulating material after attaching the first circuit board to the second circuit board.

AMENDMENT UNDER 37 C.F.R. 1.116 – EXPEDITED PROCEDURE

Page 4

Serial Number: 10/628,594

Dkt: 884.264US2 (INTEL)

Filing Date: July 28, 2003

Title: METHOD FOR MANUFACTURING AN INTERCONNECTED CIRCUIT BOARD ASSEMBLY AND SYSTEM

Assignee: Intel Corporation

Claims 14-22. (Canceled)